



4th International Workshop on Software Product Line Testing

September 10, 2007, Tokyo Japan

Call for Papers

Product Line Engineering (PLE) has become a major topic in industrial software development and many organizations have started to consider PLE as state-of-practice. One topic that needs greater emphasis is testing of product lines. Product-line testing is crucial to the successful establishment of PLE technology in an organization. The workshop aims at addressing some of the open fundamental challenges of testing in a PLE setting. How can we manage the complexity of the test space? Can we leverage our established testing tools and procedures? A particularly hard challenge for test groups in a PLE setting is keeping pace with development productivity gains. If software developers can create unique product instances 10 times faster using PLE techniques, how does the test organization keep pace without having to hire 10 times as many test engineers? Are there PLE techniques that can provide similar efficiency gains for testing as is possible for development? Without adequate answers, testing becomes the bottleneck in PLE.

Workshop Chairs:

- J. Jenny Li
Avaya Labs, USA
- John McGregor
Clemson University, USA

Program Committee:

For PLE:

- Krzysztof Czarnecki
University of Waterloo, Canada
- Robyn Lutz
Iowa State University, USA
- Frank Roessler
Avaya Labs, USA
- Tim Trew
NXP, The Netherlands
- Haiyan Zhao
Beijing University, China

For Testing:

- Myra Cohen,
University of Nebraska – Lincoln,
USA
- X Huang
Motorola Japan, Japan
- Sudipto Ghosh,
Colorado State, USA
- Antonio Magnaghi,
Yahoo Inc, USA
- Y. Nishi,
The Univ. Of Elec-Comm, Japan
- Hong Zhu,
Oxford Brookes University, UK

Important Dates:

- Submission deadline:
30 June 2007
- Notification of acceptance:
15 July 2007
- Camera ready version:
1 August 2007

In this workshop we aim at bringing together both researchers and practitioners from testing and product-line engineering on all aspects of PL testing, from designing test cases with variation points over test coverage to testing tools. We are especially interested in new ideas on techniques and tools to improve efficiency of product line testing or to reduce its costs. Our goal is to provide an opportunity to discuss innovative ideas, setting a research agenda, and starting collaborations on this topic.

Topics of interest include, but are not limited to:

- Techniques for strategic reduction in test time, test cost, and test flaws
- Supporting variation points in test cases
- Test case design techniques and test case generation for product lines
- Definition and measurement of test coverage and test effectiveness in the context of software product lines
- Minimizing redundant testing across a collection of similar products
- Reuse vs. coverage measurements for testing software product lines
- Test automation and testing tools
- Performance and reliability testing for product lines
- Traceability issues from requirements to test cases

Paper Submission:

Authors should send position papers by email to the following address:

split@biglever.com

Position papers should be no longer than 6 pages (IEEE format) and submitted as PDF-file. Submissions will be evaluated according to the relevance and originality of the work and to their ability to generate discussion among the workshop participants. The program committee is composed of experts from product-line engineering and testing. At least one testing and one product line expert will review each submission.

For more information please visit the workshop homepage at:
<http://www.biglever.com/split2007/>